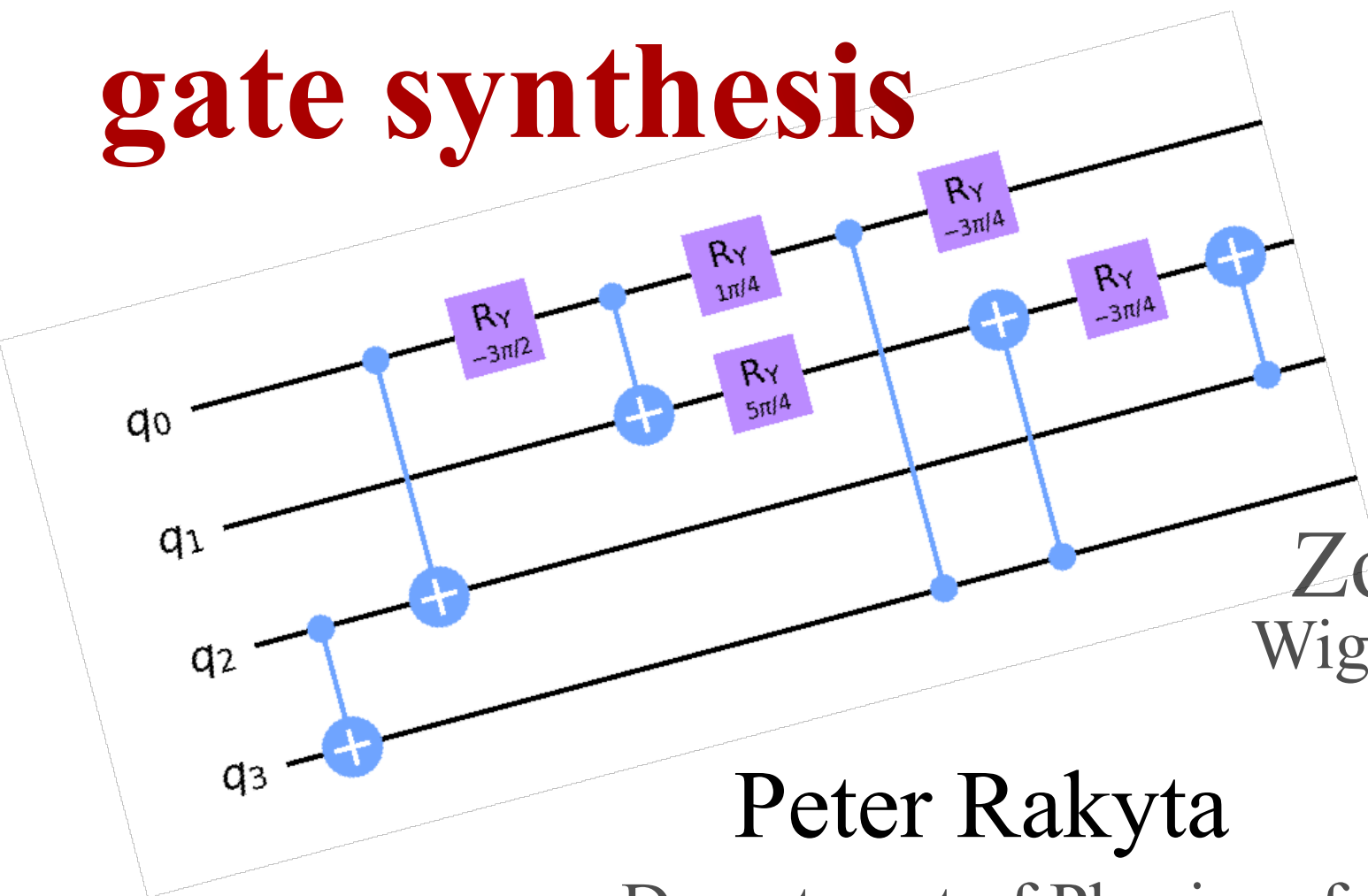


# Approximate quantum gate synthesis



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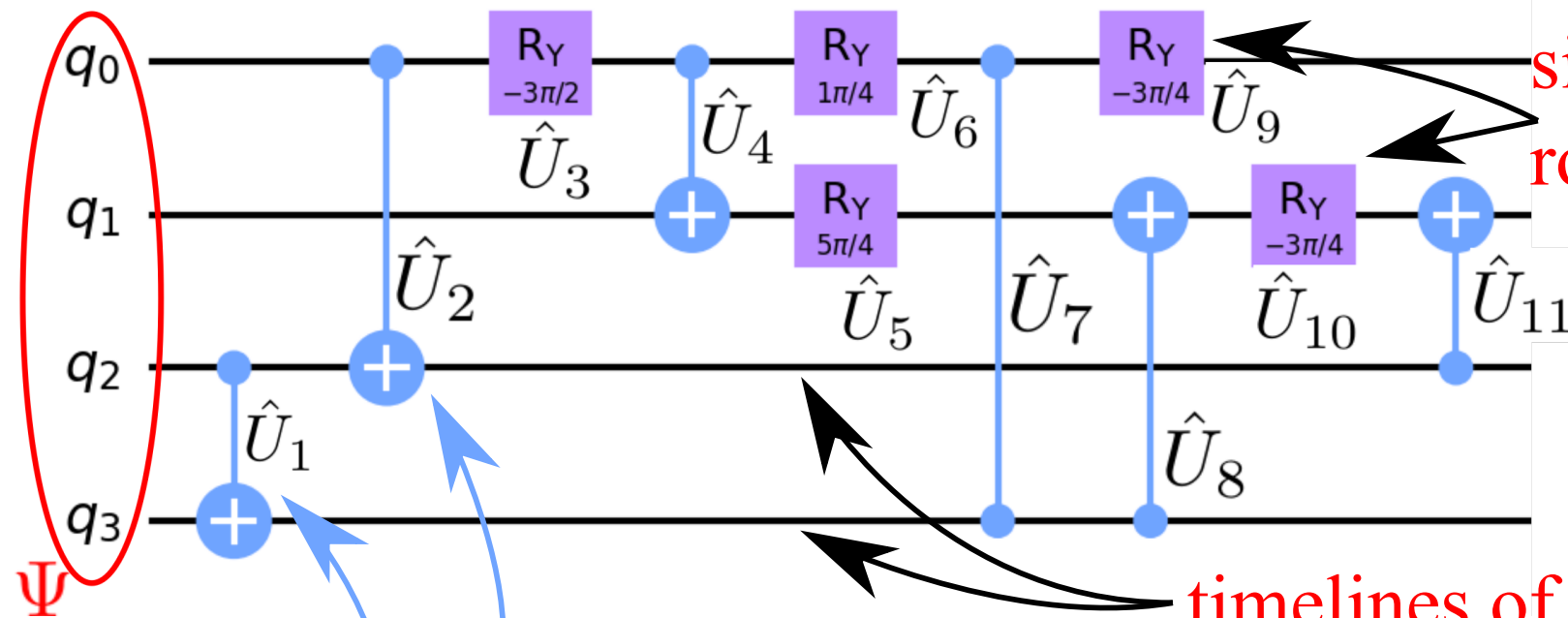
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# Quantum gate decomposition

quantum program (unitary)

$$\hat{U} = \hat{U}_{11} \cdot \hat{U}_{10} \cdot \hat{U}_9 \cdot \hat{U}_8 \cdot \hat{U}_7 \cdot \hat{U}_6 \cdot \hat{U}_5 \cdot \hat{U}_4 \cdot \hat{U}_3 \cdot \hat{U}_2 \cdot \hat{U}_1$$



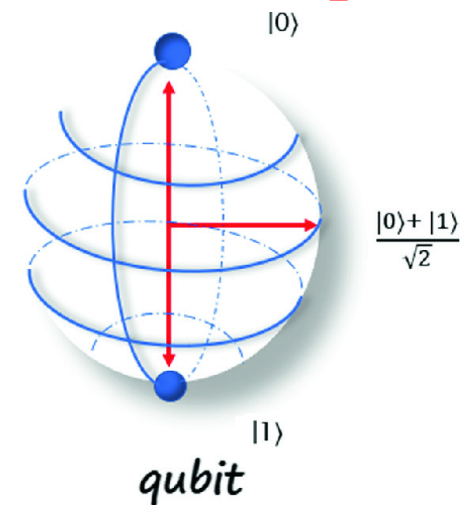
single qubit rotations

timelines of the qubits

controlled not gates



bit



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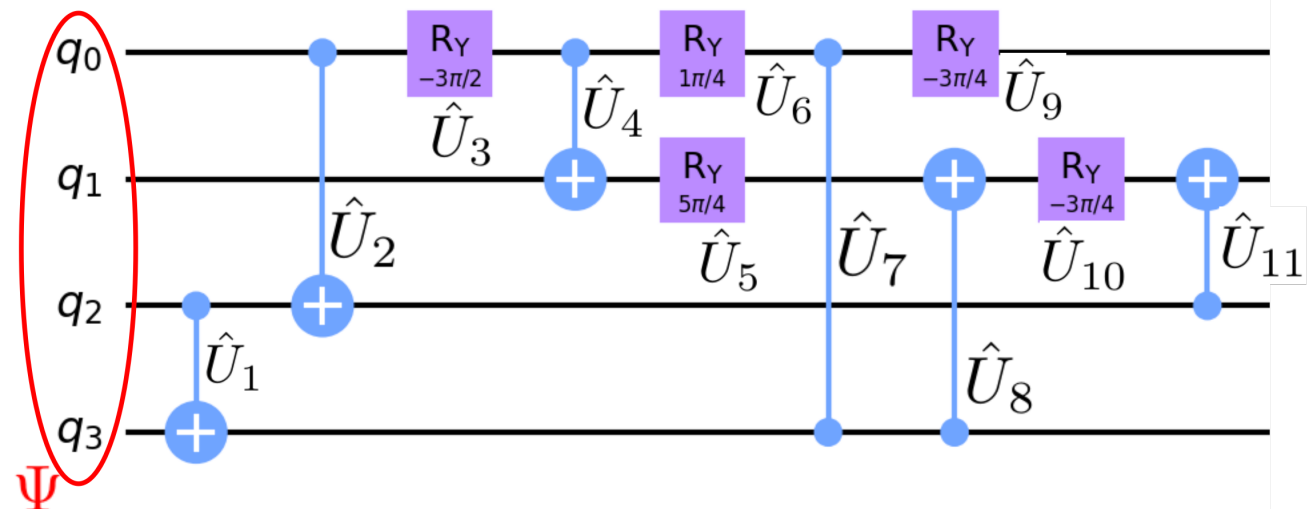


The gate decomposition is a two-fold problem of

- Combinatorial problem of placing the gates
- Optimization problem of the continuous parameters

How to measure the distance from the quantum program  $U$ ?

$$\hat{U} = \hat{U}_{11} \cdot \hat{U}_{10} \cdot \hat{U}_9 \cdot \hat{U}_8 \cdot \hat{U}_7 \cdot \hat{U}_6 \cdot \hat{U}_5 \cdot \hat{U}_4 \cdot \hat{U}_3 \cdot \hat{U}_2 \cdot \hat{U}_1$$



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# How close is an approximation to the exact one?

exact evolution:  $U$

approximate evolution:  $V$

$$|\psi(U)\rangle := U|\psi\rangle$$

$$|\psi(V)\rangle := V|\psi\rangle$$

The fidelity of the approximation:

$$\bar{F}(U, V) := \int_{\psi} |\langle \psi(V) | \psi(U) \rangle|^2 d\psi$$

average taken over the Haar distribution

The cost function of the optimization:

$$C_{HST}(U, V) = 1 - \frac{1}{d^2} |\text{Tr}(V^\dagger U)|^2$$

Hilbert-Schmidt test

dimension of the  
Hilbert-space

$$\bar{F}(U, V) = 1 - \frac{d}{d+1} C_{HST}(U, V)$$

exact decomposition:  $C_{HST}(U, V) = 0 \quad \bar{F}(U, V) = 1$



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# How close is an approximation to the exact one?

Frobenius-norm based fidelity

$$\|A\|_F = \left( \sum_{i=1}^m \sum_{j=1}^n |a_{ij}|^2 \right)^{\frac{1}{2}}$$

The cost function of the optimization:

$$f(U, V) = \frac{1}{2} \|V - U\|_F^2 = d - \text{Re} [\text{Tr}(U^\dagger V)]$$

The Fidelity:

$$\overline{F}_F(U, V) = 1 - \frac{d}{d+1} + \frac{1}{d(d+1)} (d - f(U, V))^2$$

$$\overline{F}_F(U, V) \leq \overline{F}(U, V)$$



"Best Approximate Quantum Compiling Problems"

Liam Madden (University of Colorado),

Andrea Simonetto (IBM Research Ireland)

arXiv:2106.05649

How to find a more optimal gate decomposition?

- fewest gate count?
- smallest depth?

**Available gate decomposition utilities:**

- Quantum Fast Approximate Synthesis Tool (QFAST)
- QSearch + LEAP

(Lawrence Berkeley National Laboratory)



- UniversalQCompiler (incorporated into QISKIT)

(ETH Zürich, University of York, TUM)



T|ket>: A Retargetable Compiler for NISQ Devices

(Cambridge Quantum Computing Ltd., University of Strathclyde)



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# Quantum Fast Approximate Synthesis Tool (QFAST)

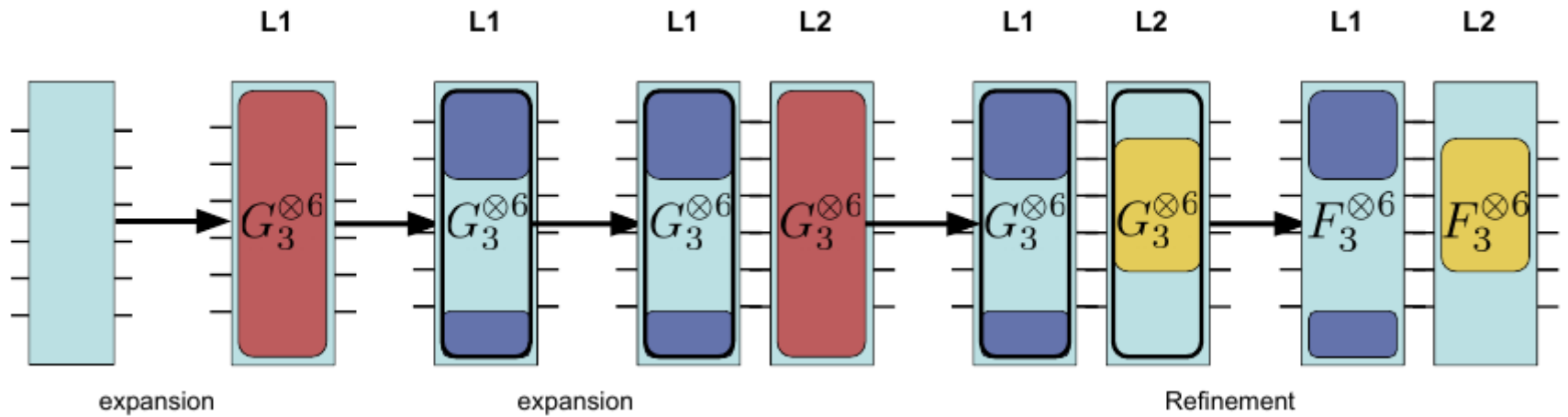


n-qubit unitary  $\rightarrow \frac{n}{2}$ -qubit unitaries  $\rightarrow \frac{n}{4}$ -qubit unitaries

$\rightarrow$  1 and 2-qubit unitaries

$$U(2^n) = \{e^{i(\alpha \cdot \sigma^{\otimes n})} \mid \alpha \in \mathbb{R}^{4^n}\}$$

$$\sigma^{\otimes n} = \{\sigma_j \otimes \sigma_k \mid \sigma_j \in \sigma, \sigma_k \in \sigma^{\otimes n-1}\} \quad \sigma = \{\sigma_i, \sigma_x, \sigma_y, \sigma_z\}$$



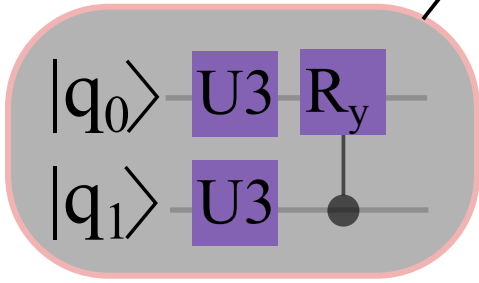
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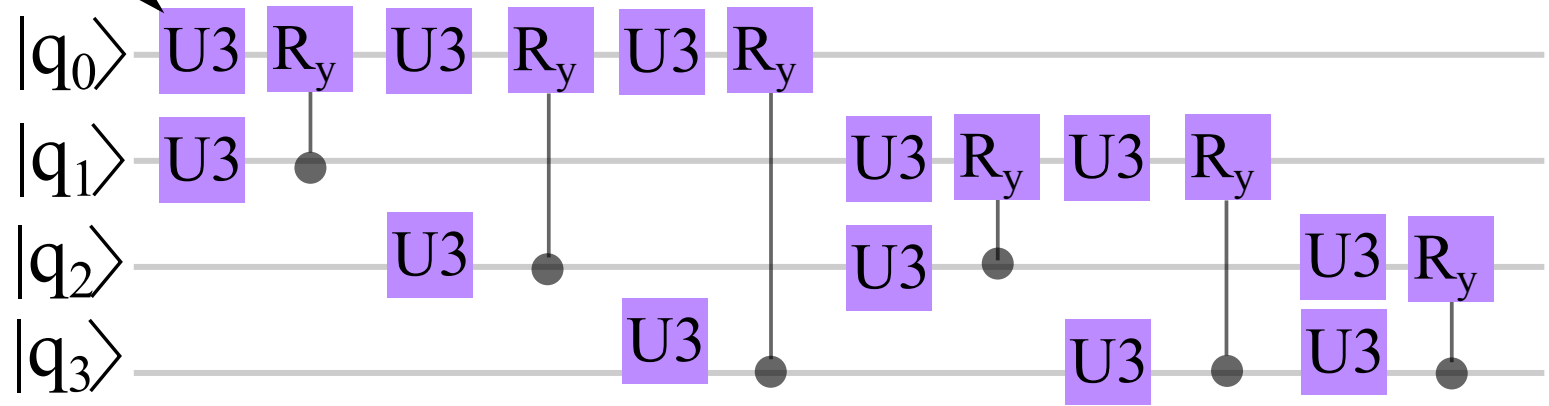


# Adaptive quantum gate decomposition (SQUANDER)

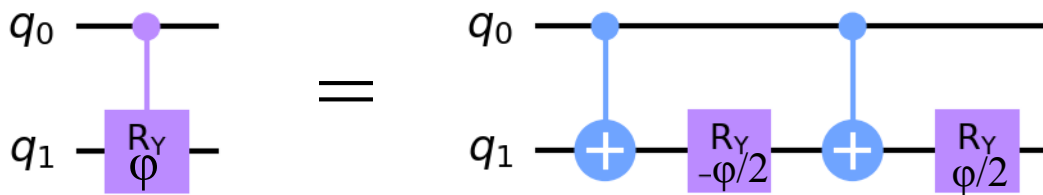
Building block



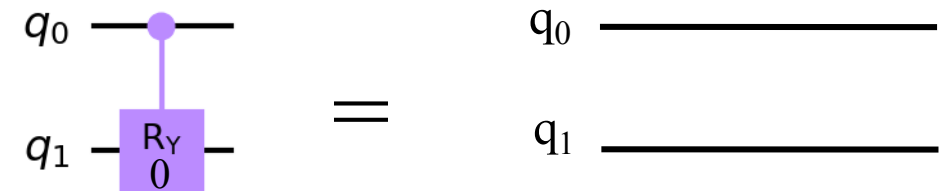
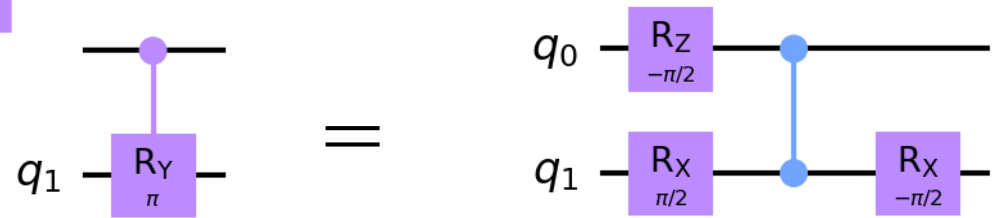
Decomposing gate structure



Expansion of controlled  $R_y$  rotations



Special case



a) In the benchmark we tested the decomposition of **3, 4 and 5-qubit unitaries from online database** containing series of circuits published as part of the **Qiskit Developer Challenge**, a public competition to design a better routing algorithm.

b) quantum circuits of well known algorithms:

Grover search,

Quantum Fourier Transformation (QFT)

Quantum Approximate Optimization Algorithm (QAOA),

Quantum variational eigensolver (VQE)



# Gate synthesis benchmark

File name	Initial	QISKIT	SQUANDER		QFAST		QSEARCH	
	<i>CNOT</i>	<i>CNOT</i>	<i>CNOT</i>	$\bar{T}$ [s]	<i>CNOT</i>	$\bar{T}$ [s]	<i>CNOT</i>	$\bar{T}$ [s]
4gt5_77	58	<b>338</b>	<b>23</b>	1293	<b>26</b>	332	-	-
4gt13_91	49	<b>187</b>	<b>23</b>	1296	<b>25</b>	732	<b>48</b>	2324
ham3_102	11	<b>15</b>	<b>6</b>	4.9	<b>7</b>	3.2	<b>8</b>	2.6
4gt5_76	46	<b>529</b>	<b>24</b>	1711	<b>29</b>	476	-	-
alu-v0_26	38	<b>204</b>	<b>23</b>	7900	<b>42</b>	912	<b>29</b>	9284
millier_11	23	<b>18</b>	<b>8</b>	7	<b>9</b>	5.4	<b>10</b>	4.5
rd32_v1_68	16	<b>66</b>	<b>9</b>	23.9	<b>13</b>	21.6	<b>13</b>	615
4mod5-v0_20	10	<b>526</b>	<b>9</b>	3650	<b>17</b>	166	<b>16</b>	14508
alu-v0_27	17	<b>212</b>	<b>17</b>	3452	<b>30</b>	674	<b>34</b>	3801
mod5mils_65	16	<b>73</b>	<b>12</b>	11162	<b>20</b>	405	-	-
ex-1_166	9	<b>20</b>	<b>9</b>	4.4	<b>8</b>	4.7	<b>8</b>	5.9
decod24-v1_41	38	<b>130</b>	<b>20</b>	2414	<b>36</b>	413	<b>24</b>	349
alu-v3_34	24	<b>237</b>	<b>25</b>	6090	<b>37</b>	1814	<b>27</b>	7834
3_17_13	17	<b>23</b>	<b>7</b>	6.5	<b>9</b>	4.2	<b>9</b>	4.3
4gt11_84	9	<b>163</b>	<b>9</b>	642	<b>20</b>	318	-	-
decod24-v0_38	23	<b>48</b>	<b>14</b>	62	<b>23</b>	58	<b>15</b>	285
4mod5-v0_19	16	<b>75</b>	<b>13</b>	701	<b>21</b>	375	-	-
4mod5-v1_22	11	<b>168</b>	<b>9</b>	962	<b>13</b>	52	<b>17</b>	82

# Gate synthesis benchmark

File name	Initial	QISKIT	SQUANDER		QFAST		QSEARCH	
	<i>CNOT</i>	<i>CNOT</i>	<i>CNOT</i>	$\bar{T}$ [s]	<i>CNOT</i>	$\bar{T}$ [s]	<i>CNOT</i>	$\bar{T}$ [s]
alu-v1_29	17	<b>240</b>	<b>19</b>	3820	<b>33</b>	801	-	-
alu-v1_28	18	<b>331</b>	<b>19</b>	2488	<b>36</b>	607	-	-
4mod5-v1_23	32	<b>74</b>	<b>13</b>	946	<b>40</b>	702	-	-
4mod5-v0_18	31	<b>671</b>	<b>15</b>	1134	<b>31</b>	266	-	-
rd32_270	36	<b>522</b>	<b>14</b>	893	<b>27</b>	627	-	-
rd32-v0_66	16	<b>66</b>	<b>10</b>	29	<b>16</b>	25	<b>13</b>	443
alu-v3_35	18	<b>249</b>	<b>20</b>	3655	<b>31</b>	1050	-	-
4gt13-v1_93	30	<b>218</b>	<b>23</b>	2408	<b>38</b>	466	<b>33</b>	21315
4mod5-v1_24	16	<b>241</b>	<b>14</b>	5081	<b>33</b>	210	<b>52</b>	3968
mod5d1_63	13	<b>76</b>	<b>13</b>	867	<b>29</b>	304	-	-
alu-v4_36	51	<b>193</b>	<b>40</b>	11090	<b>49</b>	2343	-	-
4gt11_82	18	<b>419</b>	<b>15</b>	883	<b>22</b>	698	<b>19</b>	1003
4gt5_75	38	<b>259</b>	<b>25</b>	7002	<b>37</b>	429	<b>49</b>	33246
alu-v2_33	17	<b>358</b>	<b>17</b>	2339	<b>31</b>	665	<b>23</b>	6520
4gt11_83	14	<b>151</b>	<b>13</b>	1994	<b>15</b>	98	<b>19</b>	1107
decod24-v2_43	22	<b>46</b>	<b>9</b>	93	<b>19</b>	44	<b>17</b>	1390
4gt13_92	30	<b>161</b>	<b>24</b>	1767	<b>46</b>	1830	-	-
alu-v4_37	18	<b>276</b>	<b>18</b>	3509	<b>37</b>	837	<b>32</b>	2142
mod5d2_64	25	<b>129</b>	<b>14</b>	846	<b>26</b>	104	<b>16</b>	256

optimization error:  $f < 10^{-8}$

In the 21% percent of the experiments SQUANDER decreased the number of the CNOT gates by more than 50%

In 68% of the examples the compression was more than 10%.

In exchange of larger execution time...



We have designed an adaptive circuit compression algorithm providing the fewest gatecount in the decomposition.

Aiming to reduce the execution time by:

- Using first order optimization method  
(currently second-order BFGG method is used)
- Make faster the evaluation of the cost function using data-flow engines.



# Aknowledgement

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